



PCB LAYOUT  
Ref.

REVISIONS			
LTR	DESCRIPTION	DATE	APP
A	3.23	03/22/'01	CHANG
B	15.69	04/15/'01	CHANG

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TITLE RJ45, FLAT PIN, SHIELDED  
TOP ENTRY PCB JACK

SCALE	SIZE	DWG.NO.	REV.	SHEET
		964603	8	1

DO NOT SCALE  
DRAWING

TOLERANCES UNLESS  
OTHERWISE NOTED

INCH

MM

DR.  
*M.L.HUANG*

DATE  
OCT-12 '05

THIRD ANGLE PROJECTION

LINEAR  $\frac{\text{MM}}{\text{INCH}}$

00 ± .01  
000 ± .005  
0000 ± .002

0.0 ± 0.25  
0.00 ± 0.13  
0.000 ± 0.05

CK'D  
*M.CHANG*

DATE  
OCT-12 '05

MM  
INCH

ANGLE ± 0° 30'

RADII

+ 0.40  
- 0.15

APP'D  
*W.J.YANG*

DATE  
OCT-13 '05